

MB1-L2DA

2 Channels Analog Output Module



Specification

Total Channels : 2 Channels

Resolution : 12 bits

Coding Format : 14 bits (0 ~ 16383)

Signal Resolution : 2.44mV(Voltage), 4.88uA(Current)

Registers Occupied : 2 Register (D4076 ~ D4077)

Conversion Time : Updated each scan

Accuracy : $\pm 1\%$

Max. and Min. output loading :

Voltage Output : 2K $\sim$ 1M Ω

Current Output : 0 $\sim$ 500 Ω

Output Range : 0 $\sim$ 10V(Voltage)

0 $\sim$ +20mA(Current)

Internal Power Consumption : 5V, 69mA(Max. load)

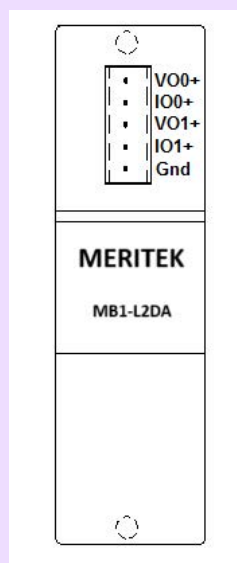
Operating Temperature : 0 $\sim$ 60 $^{\circ}\text{C}$

Storage Temperature : -20 $\sim$ 80 $^{\circ}\text{C}$

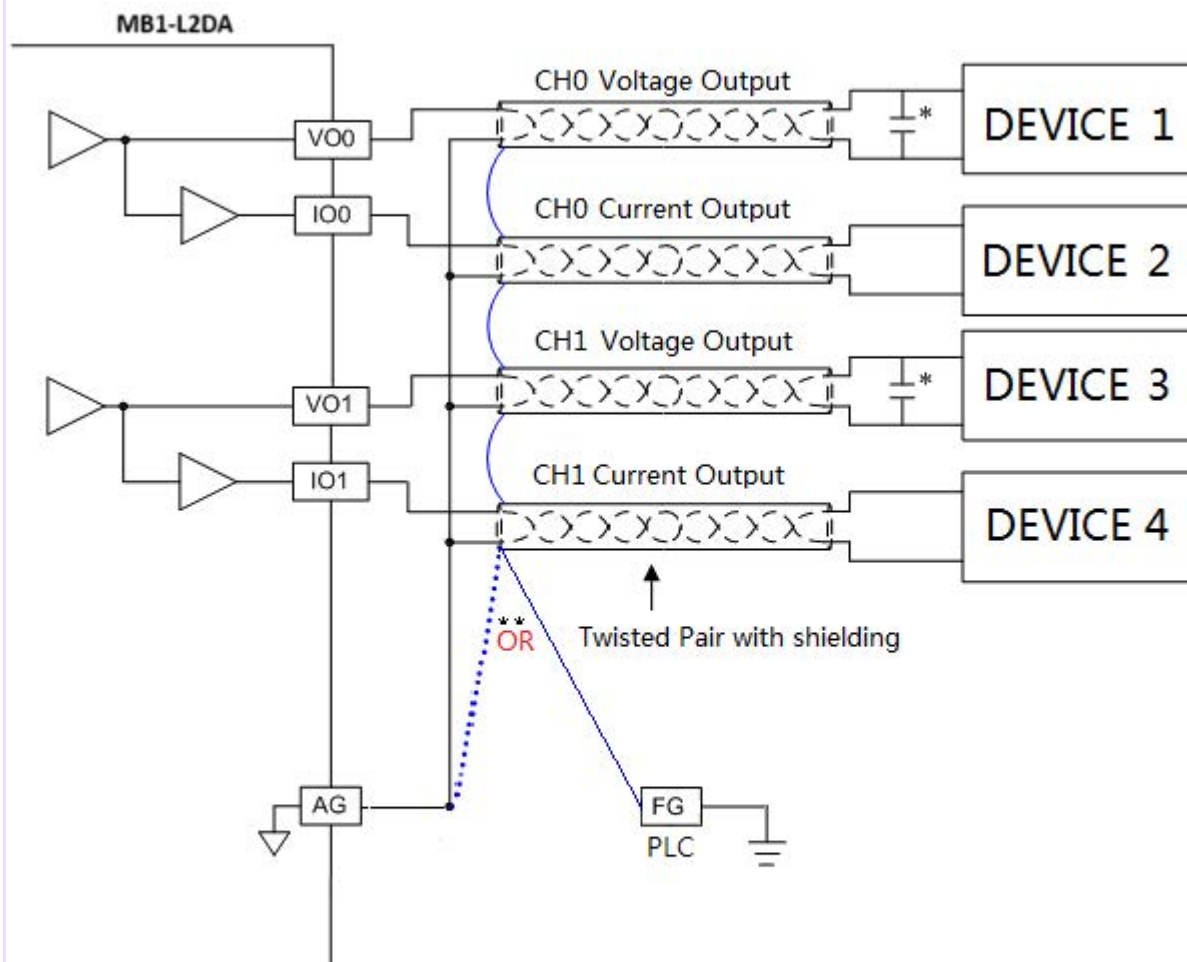
Introduction

MB1-L2DA is one of the analog output module of MERITEK MB1 series PLC. It provides 2 channels of 12-bit (coded in 14 bits) analog outputs.

Pin Diagram



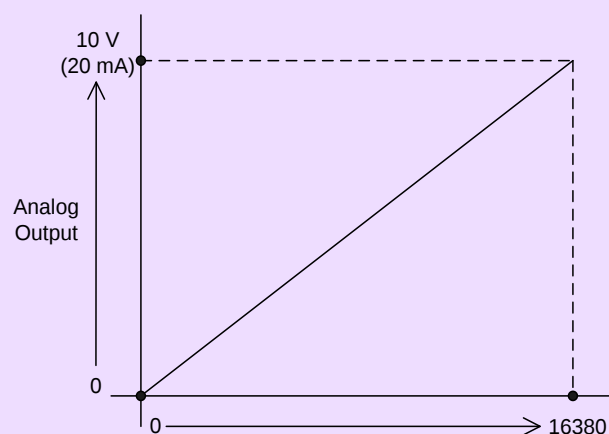
Wiring Diagram



* 0.1 ~ 0.47 uF capacitor (to filter noise) Advice to install, but not necessary !!

** If you like to have Twisted pair with shielding connect to ground , we suggest you to connect FG of PLC, if you can't connect to the FG of PLC, please connect to AG of module.

Characteristics Chart



Registers allocation Map

Channel No.		Mapped Register
Analog Output	CH0	D4076 (0 ~ 16383)
	CH1	D4077 (0 ~ 16383)

* Left Side Expansion Module has to add module manually in Winproladder.